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Docket No.: 42390.P8517

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re the application of:

Kahn, et al

164003

Serial No.: 09/228,817

Examiner: Chang, Eric

Filed: 2/14/00

Art Unit: 2185

Title: (AS AMENDED) A METHOD AND
APPARATUS FOR RESUMING
MEMORY OPERATIONS UPON EXIT
FROM FROM A LOW LATENCY
WAKE-UP LOW POWER STATEAMENDMENT AND RESPONSE TO OFFICE ACTION

Hon. Commissioner of
Patent & Trademarks
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Office Action mailed 9/9/03, applicants respectfully request entry
of the following amendment and consideration of the following remarks.

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110.00 DA

Intel Corporation
2200 Mission College Blvd.
Santa Clara, CA 95052

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Intel Legal Team

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Urgent

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Confidential

Date: 9-Jan-04

To:	Fax:	M/S:
Examiner Chang, Eric	703-746-7239	Art Unit 2185
USPTO		

From:	Fax:	M/S:
Jeffrey S Draeger	408-765-1621	SC4-203

Subject:

Application Serial No. 09/228,817

Title: A METHOD AND APPARATUS FOR RESUMING MEMORY OPERATIONS
UPON EXIT FROM FROM A LOW LATENCY WAKE-UP LOW POWER STATE

Filed 2/14/00

Docket 42390.P8517

CERTIFICATE OF TRANSMISSION

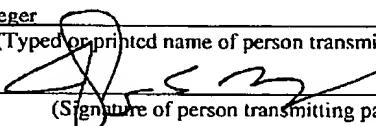
I hereby certify that this correspondence is being facsimile transmitted to the

Patent and Trademark Office on:

Date of Transmission January 9, 2004

Jeffrey S. Draeger

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